

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A voltage control device for connection to an electrical supply having an alternating supply voltage, the device comprising:
 - an input having an input voltage, said input for connection to the electrical supply;
 - an output having an output voltage;
 - means for comparing the output voltage with a predetermined voltage and generating a comparison signal;
 - means to adjust for adjusting the output voltage in response to the comparison signal, said means being connected to the input and the output;
 - whereby the output voltage is maintained substantially at the predetermined voltage.
2. (Currently Amended) A device according to claim 1, wherein the means to adjust for adjusting the output voltage comprise comprises means to delay for delaying the onset of the a rise of the output voltage within a at least one of the two half-cycles of a waveform of the output voltage.
3. (Cancelled)
4. (Currently Amended) A device according to claim 3 2, wherein the delay in the onset of the rise of the output voltage within one half-cycle is controlled independently of the delay in the onset of the rise of the output voltage within the other half-cycle.
5. (Currently Amended) A device according to any of the preceding claims claim 1, wherein the means to adjust for adjusting the output voltage comprise comprises a thyristor module.
6. (Original) A device according to claim 5, wherein the thyristor module comprises an antiparallel pair of thyristors.

7. (Currently Amended) A device according to claim 1 wherein the means to adjust for adjusting the output voltage ~~comprise comprises~~ means to reduce for reducing the amplitude of the output voltage within ~~a~~ at least one of the two half-cycles of a waveform of the output voltage.
8. (Cancelled)
9. (Currently Amended) A device according to claim 8 7, wherein the reduction in the amplitude of the output voltage within one half-cycle is controlled independently of the reduction in the amplitude of the output voltage within the other half-cycle.
10. (Currently Amended) A device according to ~~any of claim 7 to 9~~ claim 7, wherein the means to reduce for reducing the amplitude of the output voltage ~~comprise comprises~~ a variable AC transformer.
11. (Currently Amended) A device according to any preceding claim 1, further comprising a bypass switch connected across the means to adjust for adjusting the output voltage.
12. (Currently Amended) A device according to any preceding claim 1, further comprising means to vary for varying the predetermined voltage.
13. (Currently Amended) A device according to any preceding claim 1, further comprising a display for displaying at least one set-up parameters parameter and operating information.
14. (Currently Amended) A device according to any preceding claim 1, which wherein the device is powered by the input voltage.
15. (Currently Amended) A device according to any preceding claim 1, for connection to wherein the electrical supply having an alternating supply voltage is one of (i) a single phase supply voltage and (ii) a multiple phase supply voltage.
16. (Cancelled)

17. (Currently Amended) A device according to claim 16 15, wherein the multiple phase supply voltage is a three phase voltage.

18. (Currently Amended) A method of controlling an alternating voltage comprising the steps of:

providing a device having an input which has an input voltage, said input being connected to an electrical supply having an alternating supply voltage; and an output having an output voltage;

comparing the output voltage with a predetermined voltage to generate a comparison signal; and

adjusting the output voltage in response to the comparison signal whereby the output voltage is maintained substantially at the predetermined voltage.

19. (Currently Amended) A method according to claim 18 wherein adjustment of said step of adjusting the output voltage comprises delaying the onset of the a rise of the output voltage within a at least one of the two half-cycles of a waveform of the output voltage.

20. (Cancelled)

21. (Currently Amended) A method according to claim 20 19, wherein the delaying of the onset of the rise of the output voltage within one half-cycle is controlled independently of the delaying of the onset of the rise of the output voltage within the other half-cycle.

22. (Currently Amended) A method according to ~~any of claims 18 to 21~~ claim 19, wherein the delay in the onset of the rise of the output voltage is caused by a thyristor module.

23. (Currently Amended) A method according to ~~any of~~ claim 22 wherein the thyristor module comprises a pair of antiparallel thyristors.

24. (Currently Amended) A method according to claim 18 wherein ~~adjustment of said step of adjusting the output voltage comprises reduction of reducing~~ the amplitude of the output voltage within ~~a~~ at least one of the two half-cycles of a waveform of the output voltage.
25. (Cancelled)
26. (Currently Amended) A method according to claim ~~25~~ 24, wherein the reduction in the amplitude of the output voltage within one half-cycle is controlled independently of the reduction in the amplitude of the output voltage within the other half-cycle.
27. (Currently Amended) A method according to ~~any of claims 7 to 9~~ claim 24, wherein the reduction of the amplitude of the output voltage is caused by a variable AC transformer.
28. (Currently Amended) A method according to ~~any of claims 18 to 27~~ claim 18, wherein the predetermined voltage is varied.
29. (Currently Amended) A method according to ~~any of claims 18 to 28~~ claim 18, wherein the electrical supply having an alternating supply voltage is one of (i) a single phase supply voltage and (ii) a multiple phase supply voltage.
30. (Cancelled)
31. (Currently Amended) A method according to claim ~~30~~ 29, wherein the multiple phase supply voltage is a three-phase voltage.
32. (Cancelled)
33. (Cancelled)
34. (Cancelled)